activated by the at least one switch-on control signal for the purpose of evaluation and/or storage; and wherein, during the evaluation and/or storage, the at least one information signal may be or is the sole energy source for the electronic circuit.

2. (amended) The electronic circuit as claimed in claim 1, wherein the control means can generate at least one switch-off control signal after a predetermined time has elapsed after the at least one information signal arrives or when the energy converted from the at least one information signal is exhausted, wherein the signal processing means can be caused or is caused to effect a storage and to effect deactivation by the at least one switch-off control signal.

3. (amended) The electronic circuit as claimed in claim 1, wherein the information stored in the at least one ferroelectric flip-flop can be converted into at least one output signal by the signal processing means and the electronic circuit furthermore has at least one output for outputting the at least one output signal.

4. (amended) The electronic circuit as claimed in claim 1, wherein the electronic circuit furthermore has a display means for displaying the information stored in the at least one ferroelectric flip-flop.

5. (amended) The electronic circuit as claimed in claim 4, wherein the display means is concomitantly supplied by the voltage supply generated by the energy means.

6. (amended) The electronic circuit as claimed in claim 4, wherein the display means has an LCD display.

7. (amended) The electronic circuit as claimed in claim 3, wherein an external voltage supply and external control means can be connected for the outputting of the information stored in the at least one ferroelectric flip-flop by the signal processing means.

8. (amended) The electronic circuit as claimed in claim 1, wherein the at least one switch-on control signal is comprised of the following signals: an activation signal (PRECH) for activating precharge transistors of the at least one ferroelectric flip-flop; a transfer signal (PLN) for transferring the information contained in ferroelectric capacitors of the at least one ferroelectric flip-flop onto internal data lines of the at least one ferroelectric flip-flop; and a current switching signal (NSET) for switching on the voltage supply of the signal processing means.

9. (amended) The electronic circuit as claimed in claim 2, wherein the at least one switch-off control signal is comprised of the following signals: a transfer end signal (PLN); an activation signal (PRECH) for activating precharge transistors of the at least one ferroelectric flip-flop; and a current switch-off signal (NSET) for switching off the voltage supply of the signal processing means.

10. (amended) The electronic circuit as claimed in claim 8, wherein signal lines for each of the switch-on signals lead from the control means to the signal processing means.

11. (amended) The electronic circuit as claimed in claim 9, wherein signal lines for each of the switch-off signals lead from the control means to the signal processing means.

12. (amended) The electronic circuit as claimed in claim 11, characterized in that, for the transfer signal and the transfer end signal, a common transfer signal line leads from the control means to the signal processing means, the transfer signal consists in the application of a voltage to the common transfer signal line and the transfer end signal consists in the disconnection of the voltage on the common transfer signal line.

13. (amended) The electronic circuit as claimed in claim 11, characterized in that, for the current switching signal and the current switch-off signal, a common current signal line leads from the control means to the signal processing means, the current switching signal consists in the application of a voltage to the common current signal line and the current switch-off signal consists in the disconnection of the voltage on the common current signal line.

14. (amended) The electronic circuit as claimed in claim 1, wherein the signal processing circuit is a counting circuit for evaluating a plurality of information signals, which arrive successively or simultaneously, by counting the information signals that have arrived.

15. (amended) The electronic circuit as claimed in claim 14, wherein the counting circuit comprises a plurality of cascaded edge-controlled ferroelectric flip-flops, in which the at least one information signal is input into the clock input (CLK) of the first ferroelectric flip-flop of the plurality of cascaded ferroelectric flip-flops and the output (Q) of each of the ferroelectric flip-flops, except for the last, is in each case also connected to the clock input (CLK) of the ferroelectric flip-flop connected downstream.

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16. (amended) A method for storing information comprising at least one information signal or information obtained through evaluation of the at least one information signal in at least one ferroelectric flip-flop in a signal processing means, having the following steps: A: generating at least one switch-on control signal from an information signal that has arrived, and generating a voltage supply from energy contained in the at least one information signal; B: activating the signal processing means by the switch-on control signal and applying the voltage supply to the signal processing means; C: storing an information item represented by the at least one information signal by means of at least one ferroelectric flip-flop and/or evaluating an information item represented by the at least one information signal and storing the secondary information obtained through the evaluation by means of at least one ferroelectric flip-flop; D: generating a switch-off control signal after a predetermined time has elapsed after the at least one information signal arrives and/or when the energy converted from the at least one information signal is exhausted; and E: deactivating the signal processing means by the switch-off control signal.

17. (amended) The method as claimed in claim 16, wherein step B further comprises the sub-steps: B1: activating precharge transistors of the at least one ferroelectric flip-flop by applying a voltage: B2: deactivating the precharge transistors of the at least one ferroelectric flip-flop by disconnecting the voltage; B3: applying a voltage to ferroelectric capacitors of the at least one ferroelectric flip-flop for transferring the information stored in the ferroelectric capacitors to logic gates of the at least one ferroelectric flip-flop; and B4: activating the voltage supply of the logic gates of the at least one ferroelectric flip-flop.

18. (amended) The method as claimed in claim 16, wherein step E further comprises the sub-steps: E1: disconnecting a voltage present across ferroelectric capacitors of the at least

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one ferroelectric flip-flop; E2: deactivating the voltage supply of the logic gates of the at least one ferroelectric flip-flop; E3: activating precharge transistors of the at least one ferroelectric flip-flop by applying a voltage; and E4: deactivating the precharge transistors of the at least one ferroelectric flip-flop by disconnecting the voltage.

19. (amended) The method as claimed in claim 16, wherein the electronic circuit contains a plurality of ferroelectric flip-flops and the evaluation comprises a summation of the value represented by the information signal and a value already stored in the ferroelectric flip-flops.

20. (amended) The method as claimed in claim 19, wherein the summation is effected by means of a counting operation in which the plurality of ferroelectric flip-flops are cascaded in a counter arrangement and an arriving information signal increments or decrements a counter reading of the counter arrangement by the value 1.

21. (amended) The method as claimed in claim 16, wherein the information stored in the at least one ferroelectric flip-flop can be converted into at least one output signal and be output from the electronic circuit.

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22. (amended) The use of ferroelectric flip-flops for electronic circuits, wherein the electronic circuit can detect and/or evaluate information signals and results of the detection and/or evaluation can be stored in at least one ferroelectric flip-flop, characterized in that the entire energy required for the detection, processing and storage can be generated from the information signal.

23. (amended) The use as claimed in claim 22, wherein the evaluation comprises counting the arriving information signals.

24. (amended) The use as claimed in claim 23, wherein the electronic circuit can count up or count down arriving information signals.

25. (amended) The use as claimed in claim 22, wherein the electronic circuit is used in a liquid counter.

26. (amended) A liquid counter for determining the flow of liquids through a system, comprising: a sensor which can generate or generates information signals depending on a quantity of liquid flowing through the system; and an electronic circuit as claimed in claim 1 for counting the information signals generated by the sensor; wherein the information signals are the sole energy source for the electronic circuit.

In the Claims

1. (amended) 1. Electronic circuit [(1)], [having] comprising an input [(5)] for inputting at least one information signal; an energy means [(2)] for converting energy contained in the at least one information signal into a voltage supply; a control means [(3)] for generating at least one switch-on control signal when an information signal arrives; and a signal processing means [(4)] for storing an information item represented by the at least one information signal by means of at least one ferroelectric flip-flop and/or for evaluating an information item represented by the at least one information signal and storing the secondary information obtained through the evaluation by means of the at least one ferroelectric flip-flop [(26)]; wherein the signal processing means [(4)] can be activated by the at least one switch-on control signal for the purpose of evaluation and/or storage; and wherein, during the evaluation and/or storage, the at least one information signal may be or is the sole energy source for the electronic circuit [(1)].

2. (amended) The electronic circuit as claimed in claim 1, [characterized in that] wherein the control means [(3)] can generate at least one switch-off control signal after a predetermined time has elapsed after the at least one information signal arrives [and/]or when the energy converted from the at least one information signal is exhausted, wherein the signal processing means [(4)] can be caused or is caused to effect a storage and to effect deactivation by the at least one switch-off control signal.

3. (amended) The electronic circuit [(1)] as claimed in claim 1 [or 2], [characterized in that] wherein the information stored in the at least one ferroelectric flip-flop [(26)] can be converted into at least one output signal by the signal processing means [(4)] and the electronic circuit [(1)] furthermore has at least one output [(6)] for outputting the at least one output signal.

4. (amended) The electronic circuit[s] [(1)] as claimed in [one of] claim[s] 1 [to 3], [characterized in that] wherein the electronic circuit furthermore has a display means [(10)] for displaying the information stored in the at least one ferroelectric flip-flop [(26)].

5. (amended) The electronic circuit [(1)] as claimed in claim 4, [characterized in that] wherein the display means [(10)] is concomitantly supplied by the voltage supply generated by the energy means [(2)].

6. (amended) The electronic circuit [(1)] as claimed in claim 4 [or 5], [characterized in that] wherein the display means [(10)] has an LCD display [(11)].

7. (amended) The electronic circuit [(1)] as claimed in [one of] claim[s] 3 [to 6], [characterized in that] wherein an external voltage supply and external control means can be connected for the outputting of the information stored in the at least one ferroelectric flip-flop [(26)] by the signal processing means [(4)].

8. (amended) The electronic circuit [(1)] as claimed [one of] claim[s] 1 [to 7], [characterized in that] wherein the at least one switch-on control signal [has] is comprised of the following signals: an activation signal (PRECH) for activating precharge transistors [(18,19)] of the at least one ferroelectric flip-flop [(26)]; a transfer signal (PLN) for transferring the information contained in ferroelectric capacitors [(14,15)] of the at least one ferroelectric flip-flop [(26)] onto internal data lines [(22,23)] of the at least one ferroelectric flip-flop [(26)]; and a current switching signal (NSET) for switching on the voltage supply of the signal processing means [(4)].

9. (amended) The electronic circuit [(1)] as claimed in [one of] claim[s] 2 [to 8], [characterized in that] wherein the at least one switch-off control signal [has] is comprised of the following signals: a transfer end signal (PLN); an activation signal (PRECH) for activating precharge transistors [(18,19)] of the at least one ferroelectric flip-flop [(26)]; and a current switch-off signal (NSET) for switching off the voltage supply of the signal processing means [(4)].

10. (amended) The electronic circuit [(1)] as claimed in claim 8 [or 9], [characterized in that] wherein signal lines [(8)] for each of the switch-on signals lead from the control means [(3)] to the signal processing means [(4)].

11. (amended) The electronic circuit [(1)] as claimed in claim 9 [or 10], [characterized in that] wherein signal lines [(8)] for each of the switch-off signals lead from the control means [(3)] to the signal processing means [(4)].

12. (amended) The electronic circuit [(1)] as claimed in claim 11, characterized in that, for the transfer signal and the transfer end signal, a common transfer signal line leads from the control means [(3)] to the signal processing means [(4)], the transfer signal consists in the application of a voltage to the common transfer signal line and the transfer end signal consists in the disconnection of the voltage on the common transfer signal line.

13. (amended) The electronic circuit [(1)] as claimed in claim 11 [or 12], characterized in that, for the current switching signal and the current switch-off signal, a common current signal line leads from the control means [(3)] to the signal processing means [(4)], the current switching signal consists in the application of a voltage to the common current signal line and the current switch-off signal consists in the disconnection of the voltage on the common current signal line.

14. (amended) The electronic circuit [(1)] as claimed in [one of] claim[s] 1 [to 13], [characterized in that] wherein the signal processing circuit [(4)] is a counting circuit for evaluating a plurality of information signals, which arrive successively or simultaneously, by counting the information signals that have arrived.

15. (amended) The electronic circuit [(1)] as claimed in claim 14, [characterized in that] wherein the counting circuit comprises a plurality of cascaded edge-controlled ferroelectric flip-flops [(34)], in which the at least one information signal is input into the clock input (CLK) of the first ferroelectric flip-flop [(34)] of the plurality of cascaded ferroelectric flip-flops [(34)] and the output (Q) of each of the ferroelectric flip-flops [(34)], except for the last, is in each case also connected to the clock input (CLK) of the ferroelectric flip-flop [(34)] connected downstream.

16. (amended) A method for storing information [represented by] comprising at least one information signal or information obtained through evaluation of the at least one information signal in at least one ferroelectric flip-flop [(26)] in a signal processing means [(4)], having the following steps: A: generating at least one switch-on control signal from an information signal that has arrived, and generating a voltage supply from energy contained in the at least one information signal; B: activating the signal processing means [(4)] by the switch-on control signal and applying the voltage supply to the signal processing means [(4)]; C: storing an information item represented by the at least one information signal by means of at least one ferroelectric flip-flop and/or evaluating an information item represented by the at least one information signal and storing the secondary information obtained through the evaluation by means of at least one ferroelectric flip-flop [(26)]; D: generating a switch-off control signal after a predetermined time has elapsed after the at least one information signal arrives and/or when the energy converted from the at least one information signal is exhausted; and E: deactivating the signal processing means [(4)] by the switch-off control signal.

17. (amended) The method as claimed in claim 16, [characterized in that] wherein step B [has] further comprises the sub-steps: B1: activating precharge transistors [(18,19)] of the at least one ferroelectric flip-flop [(26)] by applying a voltage: B2: deactivating the precharge transistors [(18,19)] of the at least one ferroelectric flip-flop [(26)] by disconnecting the voltage; B3: applying a voltage to ferroelectric capacitors [(14,15)] of the at least one ferroelectric flip-flop [(26)] for transferring the information stored in the ferroelectric capacitors [(14,15)] to logic gates [(12,13,24,25)] of the at least one ferroelectric flip-flop [(26)]; and B4: activating the voltage supply of the logic gates [(12,13,24,25)] of the at least one ferroelectric flip-flop [(26)].

18. (amended) The method as claimed in claim 16 [or 17], [characterized in that] wherein step E [has] further comprises the sub-steps: E1: disconnecting a voltage present across ferroelectric capacitors [(14,15)] of the at least one ferroelectric flip-flop [(26)]; E2: deactivating the voltage supply of the logic gates [(12,13,24,25)] of the at least one ferroelectric flip-flop [(26)]; E3: activating precharge transistors [(18,19)] of the at least one ferroelectric flip-flop [(26)] by applying a voltage; and E4: deactivating the precharge transistors [(18,19)] of the at least one ferroelectric flip-flop [(26)] by disconnecting the voltage.

19. (amended) The method as claimed in [one of] claim[s] 16 [to 18], [characterized in that] wherein the electronic circuit [(1)] contains a plurality of ferroelectric flip-flops [(26)] and the evaluation comprises a summation of the value represented by the information signal and a value already stored in the ferroelectric flip-flops [(26)].

20. (amended) The method as claimed in claim 19, [characterized in that] wherein the summation is effected by means of a counting operation in which the plurality of ferroelectric flip-flops [(26)] are cascaded in a counter arrangement and an arriving information signal increments or decrements a counter reading of the counter arrangement by the value 1.

21. (amended) The method as claimed in [one of] claim[s] 16 [to 20], [characterized in that] wherein the information stored in the at least one ferroelectric flip-flop [(26)] can be converted into at least one output signal and be output from the electronic circuit [(1)].

22. (amend he use of ferroelectric flip-flops [(26)] ectronic circuits, wherein the electronic circuit [(1)] can detect and/or evaluate information signals and results of the detection and/or evaluation can be stored in at least one ferroelectric flip-flop [(26)], characterized in that the entire energy required for the detection, processing and storage can be generated from the information signal.

23. (amended) The use as claimed in claim 22, [characterized in that] wherein the evaluation comprises counting the arriving information signals.

24. (amended) The use as claimed in claim 23, [characterized in that] wherein the electronic circuit [(1)] can count up or count down arriving information signals.

25. (amended) The use as claimed in claim 22 [or 23], [characterized in that] wherein the electronic circuit [(1)] is used in a liquid counter.

26. (amended) A liquid counter for determining the flow of liquids through a system, [having] comprising: a sensor which can generate or generates information signals depending on a quantity of liquid flowing through the system; and an electronic circuit [(1)] as claimed in [one of] claim[s] 1 [to 14] for counting the information signals generated by the sensor; wherein the information signals are the sole energy source for the electronic circuit [(1)].